| Ref<br># | Hits   | Search Query   | DBs   | Default<br>Operator | Plurals | Time Stamp       |
|----------|--------|--|---|---------------------|---------|------------------|
| L4       | 742215 | 711/103.ccls. (checksum or check adj sum) address                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/10/05 09:42 |
| L5       | 91     | 711/103.ccls. (checksum or check adj sum) address                            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND                 | OFF     | 2005/10/05 09:42 |
| L6       | 61     | 711/103.ccls. (checksum or check adj sum) address updat\$3                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND                 | OFF     | 2005/10/05 09:42 |
| L7       | 342    | 711/103.ccls. logic (cpu or processor) compar\$3                             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND                 | OFF     | 2005/10/05 11:16 |
| L8       | 202    | 711/103.ccls. logic (cpu or processor) compar\$3 updat\$3                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND                 | OFF     | 2005/10/05 11:16 |
| L9       | 20     | 711/103.ccls. logic (cpu or processor) with compar\$3 updat\$3               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND                 | OFF     | 2005/10/05 11:16 |
| S15<br>8 | 396874 | flash adj rom updat\$3   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | OFF     | 2005/09/30 09:11 |
| S15<br>9 | 316    | flash adj rom updat\$3 memory<br>(checksum or "check sum" or<br>"check-sum") | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND                 | OFF     | 2005/09/30 09:12 |

| S16<br>0 | 72  | flash adj rom updat\$3 memory<br>(checksum or "check sum" or<br>"check-sum") eprom | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | .2005/09/30 09:12 |
|----------|-----|--|---|-----|-----|-------------------|
| S16<br>1 | 934 | flash adj rom updat\$3 logic<br>compar\$4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 10:54  |
| S16<br>2 | 535 | flash adj rom updat\$3 logic<br>compar\$4 address\$3 module                        | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 10:54  |
| S16<br>3 | 225 | flash adj rom updat\$3 logic<br>compar\$4 address\$3 same module                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 11:25  |
| S16<br>4 | 304 | flash adj rom logic compar\$4<br>address\$3 BIOS                                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 11:37  |
| S16<br>5 | 70  | flash adj rom logic compar\$4<br>address\$3 BIOS eprom                             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 13:02  |
| S16<br>6 | 428 | flash adj rom information register<br>BIOS   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 13:31  |
| S16<br>7 | 200 | flash adj rom information same<br>register BIOS                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 14:19  |

| S16<br>8 | 1468  | 711/103.ccls.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 14:27 |
|----------|-------|---|---|-----|-----|------------------|
| S16<br>9 | 88    | 711/103.ccls. flash adj rom                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 14:30 |
| S17<br>0 | 786   | 711/103.ccls. updat\$3                                    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 14:30 |
| S17<br>1 | 421   | 711/103.ccls. updat\$3 flash rom                          | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 14:30 |
| S17<br>2 | 58    | 711/103.ccls. updat\$3 flash adj rom                      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/09/30 14:30 |
| S17<br>3 | 54409 | address\$3 and module and stor\$3 and updat\$3            | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 09:57 |
| S17<br>4 | 3737  | address\$3 and module and stor\$3<br>and updat\$3 bios    | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 09:57 |
| S17<br>5 | 2521  | address\$3 and module and stor\$3 and updat\$3 bios flash | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 09:58 |

| S17<br>6 | 2374   | address\$3 and module and stor\$3 same information and updat\$3 bios flash                             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 09:58 |
|----------|--------|--|---|-----|-----|------------------|
| S17<br>7 | 236    | address\$3 and module and stor\$3 same information and updat\$3 bios flash (checksum or check adj sum) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 09:58 |
| S17<br>8 | 154912 | 711/103.ccls. updat\$3 and address   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR  | OFF | 2005/10/03 11:33 |
| S17<br>9 | 686    | 711/103.ccls. updat\$3 address   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 11:35 |
| S18<br>0 | 711    | 711/103.ccls. updat\$3 address\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 11:35 |
| S18<br>1 | 610    | 711/103.ccls. updat\$3 address\$3 flash  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 11:35 |
| S18<br>2 | 468    | 711/103.ccls. updat\$3 address\$3 compar\$4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 11:36 |
| S18<br>3 | 284    | 711/103.ccls. updat\$3 address\$3 compar\$4 rom  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 11:36 |

| 1 1 | 711/103.ccls. updat\$3 address\$3 compar\$4 flash rom | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | AND | OFF | 2005/10/03 11:37 |
|-----|---|---|-----|-----|------------------|
|-----|---|---|-----|-----|------------------|